Table 2.1.3.1.2.1-1. Access Channel Modulation Parameters for Spreading Rate 1

	Data Rate (bps)	
Parameter	**4,800**	**Units**
PN Chip Rate	1.2288	Mcps
Code Rate	1/3	bits/code symbol
Code Symbol Repetition	2	repeated code symbols/code symbol
Repeated Code Symbol Rate	28,800	sps
Modulation	6	repeated code symbols/modulation symbol
Modulation Symbol Rate	4800	sps
Walsh Chip Rate	307.20	kcps
Modulation Symbol Duration	208.33	μs
PN Chips/Repeated Code Symbol	42.67	PN chips/repeated code symbol
PN Chips/Modulation Symbol	256	PN chips/modulation symbol
Transmit Duty Cycle	100.0	%
PN Chips/Walsh Chip	4	PN chips/Walsh chip

Table 2.1.3.1.2.1-2. Enhanced Access Channel Modulation Parameters for Spreading Rate 1

	Data Rate (bps)			
Parameter	**9,600**	**19,200**	**38,400**	**Units**
PN Chip Rate	1.2288	1.2288	1.2288	Mcps
Code Rate	1/4	1/4	1/4	bits/code symbol
Code Symbol Repetition	4	2	1	repeated code symbols/code symbol
Modulation Symbol Rate	153,600	153,600	153,600	sps
Walsh Length	8	8	8	PN chips
Number of Walsh Function Repetitions per Modulation Symbol	1	1	1	Walsh functions/ modulation symbol
Transmit Duty Cycle	100.0	100.0	100.0	%
Processing Gain	128	64	32	PN chips/bit

Note: The Enhanced Access header uses the 9600 bps data rate only, while the Enhanced Access data uses 9600, 19200, and 38400 bps data rates.

Table 2.1.3.1.2.1-3. Reverse Common Control Channel Modulation Parameters for Spreading Rate 1

	Data Rate (bps)			
Parameter	**9,600**	**19,200**	**38,400**	**Units**
PN Chip Rate	1.2288	1.2288	1.2288	Mcps
Code Rate	1/4	1/4	1/4	bits/code symbol
Code Symbol Repetition	4	2	1	repeated code symbols/code symbol
Modulation Symbol Rate	153,600	153,600	153,600	sps
Walsh Length	8	8	8	PN chips
Number of Walsh Function Repetitions per Modulation Symbol	1	1	1	Walsh functions/ modulation symbol
Transmit Duty Cycle	100.0	100.0	100.0	%
Processing Gain	128	64	32	PN chips/bit

Table 2.1.3.1.2.1-4. Reverse Packet Data Control Channel Modulation Parameters

	Data Rate (bps)	
Parameter	**700**	**Units**
PN Chip Rate	1.2288	Mcps
Code Rate	7/64	bits/code symbol
Code Symbol Repetition	3	repeated code symbols/code symbol
Modulation Symbol Rate	19,200	sps
Walsh Length	64	PN chips
Number of Walsh Function Repetitions per Modulation Symbol	1	Walsh functions/ modulation symbol
Transmit Duty Cycle	100.0	%
Processing Gain	1755.43	PN chips/bit

Table 2.1.3.1.2.1-5. Reverse Request Channel Modulation Parameters

	Data Rate (bps)	
Parameter	**3,200**	**Units**
PN Chip Rate	1.2288	Mcps
Code Rate	1/4	bits/code symbol
Code Symbol Repetition	6	repeated code symbols/code symbol
Modulation Symbol Rate	76,800	sps
Walsh Length	16	PN chips
Number of Walsh Function Repetitions per Modulation Symbol	1	Walsh functions/ modulation symbol
Transmit Duty Cycle	100.0	%
Processing Gain	384	PN chips/bit

Table 2.1.3.1.2.1-6. Reverse Dedicated Control Channel Modulation Parameters for Radio Configuration 3

	Data Rate (bps)	
Parameter	**9,600**	**Units**
PN Chip Rate	1.2288	Mcps
Code Rate	1/4	bits/code symbol
Code Symbol Repetition	2	repeated code symbols/code symbol
Modulation Symbol Rate	76,800	sps
Walsh Length	16	PN chips
Number of Walsh Function Repetitions per Modulation Symbol	1	Walsh functions/ modulation symbol
Transmit Duty Cycle	100.0	%
Processing Gain	128	PN chips/bit

Note: If flexible data rates are supported, repetition and puncturing can be used to support data rates not specified in this table.

Table 2.1.3.1.2.1-7. Reverse Dedicated Control Channel Modulation Parameters for Radio Configuration 4

	Data Rate (bps)		
Parameter	**9,600**	**14,400**	**Units**
PN Chip Rate	1.2288	1.2288	Mcps
Code Rate	1/4	1/4	bits/code symbol
Code Symbol Repetition	2	2	repeated code symbols/code symbol
Puncturing Rate	1	16/24	modulation symbols/repeated code symbol
Modulation Symbol Rate	76,800	76,800	sps
Walsh Length	16	16	PN chips
Number of Walsh Function Repetitions per Modulation Symbol	1	1	Walsh functions/ modulation symbol
Transmit Duty Cycle	100.0	100.0	%
Processing Gain	128	85.33	PN chips/bit

Notes:

1. The 9600 bps data rate is used for 5 ms frames and the 14400 bps data rate is used for 20 ms frames.
2. If flexible data rates are supported, repetition and puncturing can be used to support data rates not specified in this table.

Table 2.1.3.1.2.1-8. Reverse Acknowledgment Channel Modulation Parameters

	Data Rate (bps)	
Parameter	**800**	**Units**
PN Chip Rate	1.2288	Mcps
Code Rate	1/1	bits/code symbol
Code Symbol Repetition	24	repeated code symbols/code symbol
Modulation Symbol Rate	19,200	sps
Walsh Length	64	PN chips
Number of Walsh Function Repetitions per Modulation Symbol	1	Walsh functions/ modulation symbol
Processing Gain	1,536	PN chips/bit

Note: The code symbol repetition and processing gain are multiplied by a factor of 1, 2, or 4, and the data rate is divided by the same factor (see [3]).

Table 2.1.3.1.2.1-9. Reverse Channel Quality Indicator Channel Modulation Parameters

	Data Rate (bps)		
Parameter	**800**	**3,200**	**Units**
PN Chip Rate	1.2288	1.2288	Mcps
Code Rate	1/1	4/12	bits/code symbol
Code Symbol Repetition	12	1	repeated code symbols/code symbol
Modulation Symbol Rate	76,800	76,800	sps
Walsh Length	16	16	PN chips
Number of Walsh Function Repetitions per Modulation Symbol	1	1	Walsh functions/ modulation symbol
Processing Gain	1,536	384	PN chips/bit

Note: The code symbol repetition and processing gain is actually multiplied by a factor of 1, 2, or 4, and the data rate is divided by the same factor (see [3]).

Table 2.1.3.1.2.1-10. Reverse Fundamental Channel and Reverse Supplemental Code Channel Modulation Parameters for Radio Configuration 1

	Data Rate (bps)				
Parameter	**9,600**	**4,800**	**2,400**	**1,200**	**Units**
PN Chip Rate	1.2288	1.2288	1.2288	1.2288	Mcps
Code Rate	1/3	1/3	1/3	1/3	bits/code symbol
Code Symbol Repetition	1	2	4	8	repeated code symbols/code symbol
Repeated Code Symbol Rate	28,800	28,800	28,800	28,800	sps
Modulation	6	6	6	6	repeated code symbols/modulation symbol
Modulation Symbol Rate	4,800	4,800	4,800	4,800	sps
Walsh Chip Rate	307.20	307.20	307.20	307.20	kcps
Modulation Symbol Duration	208.33	208.33	208.33	208.33	µs
PN Chips/Repeated Code Symbol	42.67	42.67	42.67	42.67	PN chips/repeated code symbol
PN Chips/Modulation Symbol	256	256	256	256	PN chips/modulation symbol
PN Chips/Walsh Chip	4	4	4	4	PN chips/Walsh chip
Transmit Duty Cycle	100.0	50.0	25.0	12.5	%
Processing Gain	128	128	128	128	PN chips/bit

Note: The 1200, 2400, and 4800 bps data rates are applicable to the Reverse Fundamental Channel only.

Table 2.1.3.1.2.1-11. Reverse Fundamental Channel and Reverse Supplemental Code Channel Modulation Parameters for Radio Configuration 2

	Data Rate (bps)				
Parameter	**14,400**	**7,200**	**3,600**	**1,800**	**Units**
PN Chip Rate	1.2288	1.2288	1.2288	1.2288	Mcps
Code Rate	1/2	1/2	1/2	1/2	bits/code symbol
Code Symbol Repetition	1	2	4	8	repeated code symbols/code symbol
Repeated Code Symbol Rate	28,800	28,800	28,800	28,800	sps
Modulation	6	6	6	6	repeated code symbols/modulation symbol
Modulation Symbol Rate	4,800	4,800	4,800	4,800	sps
Walsh Chip Rate	307.20	307.20	307.20	307.20	kcps
Modulation Symbol Duration	208.33	208.33	208.33	208.33	μs
PN Chips/Repeated Code Symbol	42.67	42.67	42.67	42.67	PN chips/repeated code symbol
PN Chips/Modulation Symbol	256	256	256	256	PN chips/modulation symbol
PN Chips/Walsh Chip	4	4	4	4	PN chips/Walsh chip
Transmit Duty Cycle	100.0	50.0	25.0	12.5	%
Processing Gain	85.33	85.33	85.33	85.33	PN chips/bit

Note: The 1800, 3600, and 7200 bps data rates are applicable to the Reverse Fundamental Channel only.

Table 2.1.3.1.2.1-12. Reverse Fundamental Channel and Reverse Supplemental Channel Modulation Parameters for 20 ms Frames for Radio Configuration 3

	Data Rate (bps)				
Parameter	**9,600 × N**	**4,800**	**2,700**	**1,500**	**Units**
PN Chip Rate	1.2288	1.2288	1.2288	1.2288	Mcps
Code Rate	1/4 (N < 32) 1/2 (N = 32)	1/4	1/4	1/4	bits/code symbol
Code Symbol Repetition	2 (N = 1) 1 (N > 1)	4	8	16	repeated code symbols/code symbol
Puncturing Rate	1	1	8/9	4/5	interleaver symbols/repeated code symbol
Modulation Symbol Rate	76,800 (N ≤2) 38,400 × N (N = 4 or 8) 614,400 (N ≥ 16)	76,800	76,800	76,800	sps
Walsh Length	For Reverse Fundamental Channel: 16 For Reverse Supplemental Channel: 8, 4, or 2 (N ≤4) 4 or 2 (N = 8) 2 (N ≥ 16)	16 (Reverse Fundamental Channel) 8, 4, or 2 (Reverse Supplemental Channel)			PN chips
Number of Walsh Function Repetitions per Modulation Symbol	For Reverse Fundamental Channel: 1 For Reverse Supplemental Channel: 2, 4, or 8 (N ≤2) 1, 2, or 4 (N = 4) 1 or 2 (N = 8) 1 (N ≥ 16)	1 (Reverse Fundamental Channel) 2, 4, or 8 (Reverse Supplemental Channel)			Walsh functions/ modulation symbol
Transmit Duty Cycle	100.0	100.0	100.0	100.0 or 50.0	%
Processing Gain	128/N	256	455.1	819.2	PN chips/bit

Notes:

1. N = 1, 2, 4, 8, 16, or 32, which yields data rates of 9600, 19200, 38400, 76800, 153600, or 307200 bps, respectively.
2. If variable-rate Reverse Supplemental Channel operation, flexible reverse link data rates, or both are supported, the parameters are determined from the specified number of channel bits per frame, the maximum assigned number of channel bits per frame for the Reverse Fundamental Channel or the Reverse Supplemental Channel, and the specified frame quality indicator length.
3. The 50% transmit duty cycle at 1500 bps data rate corresponds to the Reverse Fundamental Channel gating.

Table 2.1.3.1.2.1-13. Reverse Supplemental Channel Modulation Parameters for 40 ms Frames for Radio Configuration 3

	Data Rate (bps)				
Parameter	**9,600 × N**	**4,800**	**2,400**	**1,350**	**Units**
PN Chip Rate	1.2288	1.2288	1.2288	1.2288	Mcps
Code Rate	1/4 (N < 16) 1/2 (N = 16)	1/4	1/4	1/4	bits/code symbol
Code Symbol Repetition	1	2	4	8	repeated code symbols/code symbol
Puncturing Rate	1	1	1	8/9	interleaver symbols/ repeated code symbol
Modulation Symbol Rate	38,400 × N (N < 8) 307,200 (N ≥ 8)	38,400	38,400	38,400	sps
Walsh Length	8, 4, or 2 (N < 8) 4 or 2 (N ≥ 8)	8, 4, or 2	8, 4, or 2	8, 4, or 2	PN chips
Number of Walsh Function Repetitions per Modulation Symbol	4, 8, or 16 (N = 1) 2, 4, or 8 (N = 2) 1, 2, or 4 (N = 4) 1 or 2 (N ≥ 8)	4, 8, or 16	4, 8, or 16	4, 8, or 16	Walsh functions/ modulation symbol
Transmit Duty Cycle	100.0	100.0	100.0	100.0	%
Processing Gain	128/N	256	512	910.22	PN chips/bit

Notes:

1. N = 1, 2, 4, 8, or 16, which yields data rates of 9600, 19200, 38400, 76800, or 153600 bps, respectively.
2. If variable-rate Reverse Supplemental Channel operation, flexible reverse link data rates, or both are supported, the parameters are determined from the specified number of channel bits per frame, the maximum assigned number of channel bits per frame for the Reverse Supplemental Channel, and the specified frame quality indicator length.

Table 2.1.3.1.2.1-14. Reverse Supplemental Channel Modulation Parameters for 80 ms Frames for Radio Configuration 3

	Data Rate (bps)				
Parameter	**9,600 × N**	**4,800**	**2,400**	**1,200**	**Units**
PN Chip Rate	1.2288	1.2288	1.2288	1.2288	Mcps
Code Rate	1/4 (N < 8) 1/2 (N = 8)	1/4	1/4	1/4	bits/code symbol
Code Symbol Repetition	1	1	2	4	repeated code symbols/code symbol
Modulation Symbol Rate	38,400 × N (N < 4) 153,600 (N ≥ 4)	19,200	19,200	19,200	sps
Walsh Length	8, 4, or 2	8, 4, or 2	8, 4, or 2	8, 4, or 2	PN chips
Number of Walsh Function Repetitions per Modulation Symbol	4, 8, or 16 (N = 1) 2, 4, or 8 (N = 2) 1, 2, or 4 (N ≥ 4)	8, 16, or 32	8, 16, or 32	8, 16, or 32	Walsh functions/ modulation symbol
Transmit Duty Cycle	100.0	100.0	100.0	100.0	%
Processing Gain	128/N	256	512	1024	PN chips/bit

Notes:

1. N = 1, 2, 4, or 8, which yields data rates of 9600, 19200, 38400, or 76800 bps, respectively.
2. If variable-rate Reverse Supplemental Channel operation, flexible reverse link data rates, or both are supported, the parameters are determined from the specified number of channel bits per frame, the maximum assigned number of channel bits per frame for the Reverse Supplemental Channel, and the specified frame quality indicator length.

Table 2.1.3.1.2.1-15. Reverse Fundamental Channel and Reverse Supplemental Channel Modulation Parameters for 20 ms Frames for Radio Configuration 4

	Data Rate (bps)				
Parameter	**14,400 × N**	**7,200**	**3,600**	**1,800**	**Units**
PN Chip Rate	1.2288	1.2288	1.2288	1.2288	Mcps
Code Rate	1/4	1/4	1/4	1/4	bits/code symbol
Code Symbol Repetition	2 (N = 1) 1 (N > 1)	4	8	16	repeated code symbols/code symbol
Puncturing Rate	16/24 (N = 1) 8/12 (N > 1)	16/24	16/24	16/24	interleaver symbols/ repeated code symbol
Modulation Symbol Rate	76,800 (N = 1) 38,400 × N (N ≥ 2)	76,800	76,800	76,800	sps
Walsh Length	For Reverse Fundamental Channel: 16 For Reverse Supplemental Channel: 8, 4, or 2 (N ≤4) 4 or 2 (N = 8) 2 (N = 16)	16 (Reverse Fundamental Channel) 8, 4, or 2 (Reverse Supplemental Channel)			PN chips
Number of Walsh Function Repetitions per Modulation Symbol	For Reverse Fundamental Channel: 1 For Reverse Supplemental Channel: 2, 4, or 8 (N ≤2) 1, 2, or 4 (N = 4) 1 or 2 (N = 8) 1 (N = 16)	1 (Reverse Fundamental Channel) 2, 4, or 8 (Reverse Supplemental Channel)			Walsh functions/ modulation symbol
Transmit Duty Cycle	100.0	100.0	100.0	100.0 or 50.0	%
Processing Gain	85.33/N	170.67	341.33	682.67	PN chips/bit

Notes:

1. N = 1, 2, 4, 8, or 16, which yields data rates of 14400, 28800, 57600, 115200, or 230400 bps, respectively.
2. If variable-rate Reverse Supplemental Channel operation, flexible reverse link data rates, or both are supported, the parameters are determined from the specified number of channel bits per frame, the maximum assigned number of channel bits per frame for the Reverse Fundamental Channel or the Reverse Supplemental Channel, and the specified frame quality indicator length.
3. The 50% transmit duty cycle at 1800 bps data rate corresponds to the Reverse Fundamental Channel gating.

Table 2.1.3.1.2.1-16. Reverse Supplemental Channel Modulation Parameters for 40 ms Frames for Radio Configuration 4

	Data Rate (bps)				
Parameter	**14,400 × N**	**7,200**	**3,600**	**1,800**	**Units**
PN Chip Rate	1.2288	1.2288	1.2288	1.2288	Mcps
Code Rate	1/4	1/4	1/4	1/4	bits/code symbol
Code Symbol Repetition	1	2	4	8	repeated code symbols/code symbol
Puncturing Rate	8/12	16/24	16/24	16/24	interleaver symbols/repeated code symbol
Modulation Symbol Rate	38,400 × N	38,400	38,400	38,400	sps
Walsh Length	8, 4, or 2 (N ≤4) 4 or 2 (N = 8)	8, 4, or 2	8, 4, or 2	8, 4, or 2	PN chips
Number of Walsh Function Repetitions per Modulation Symbol	4, 8, or 16 (N = 1) 2, 4, or 8 (N = 2) 1, 2, or 4 (N = 4) 1 or 2 (N = 8)	4, 8, or 16	4, 8, or 16	4, 8, or 16	Walsh functions/ modulation symbol
Transmit Duty Cycle	100.0	100.0	100.0	100.0	%
Processing Gain	85.33/N	170.67	341.33	682.67	PN chips/bit

Notes:

1. N = 1, 2, 4, or 8, which yields data rates of 14400, 28800, 57600, or 115200 bps, respectively.
2. If variable-rate Reverse Supplemental Channel operation, flexible reverse link data rates, or both are supported, the parameters are determined from the specified number of channel bits per frame, the maximum assigned number of channel bits per frame for the Reverse Supplemental Channel, and the specified frame quality indicator length.

Table 2.1.3.1.2.1-17. Reverse Supplemental Channel Modulation Parameters for 80 ms Frames for Radio Configuration 4

	Data Rate (bps)				
Parameter	**14,400 × N**	**7,200**	**3,600**	**1,800**	**Units**
PN Chip Rate	1.2288	1.2288	1.2288	1.2288	Mcps
Code Rate	1/4	1/4	1/4	1/4	bits/code symbol
Code Symbol Repetition	1	1	2	4	repeated code symbols/code symbol
Puncturing Rate	8/12	8/12	16/24	16/24	interleaver symbols/ repeated code symbol
Modulation Symbol Rate	38,400 × N	19,200	19,200	19,200	sps
Walsh Length	8, 4, or 2	8, 4, or 2	8, 4, or 2	8, 4, or 2	PN chips
Number of Walsh Function Repetitions per Modulation Symbol	4, 8, or 16 (N = 1) 2, 4, or 8 (N = 2) 1, 2, or 4 (N = 4)	8, 16, or 32	8, 16, or 32	8, 16, or 32	Walsh functions/ modulation symbol
Transmit Duty Cycle	100.0	100.0	100.0	100.0	%
Processing Gain	85.33/N	170.67	341.33	682.67	PN chips/bit

Notes:

1. N = 1, 2, or 4, which yields data rates of 14400, 28800, or 57600 bps, respectively.
2. If variable-rate Reverse Supplemental Channel operation, flexible reverse link data rates, or both are supported, the parameters are determined from the specified number of channel bits per frame, the maximum assigned number of channel bits per frame for the Reverse Supplemental Channel, and the specified frame quality indicator length.

Table 2.1.3.1.2.1-18. Reverse Fundamental Channel Modulation Parameters for 5 ms Frames

	Data Rate (bps)	
Parameter	**9,600**	**Units**
PN Chip Rate	1.2288	Mcps
Code Rate	1/4	bits/code symbol
Code Symbol Repetition	2	repeated code symbols/code symbol
Puncturing Rate	1	interleaver symbols/repeated code symbol
Modulation Symbol Rate	76,800	sps
Walsh Length	16	PN chips
Number of Walsh Function Repetitions per Modulation Symbol	1	Walsh functions/ modulation symbol
Transmit Duty Cycle	100.0	%
Processing Gain	128	PN chips/bit